TAMILNADU GOVERNMENT POLYTECHNIC COLLEGE, (AUTONOMOUS), MADURAI- 11

DIPLOMA IN ENGINEERING / TECHNOLOGY SYLLABUS DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

N - 20-SCHEME

(Implemented from the Academic year 2021-2022 onwards)

FOURTH SEMESTER (FULL TIME)

Subject		HOURS PER WEEK				
Code	SUBJECT	Theory	Drawing	Practical	Total	
Code		hours	hours	hours	hours	
4030410	Electrical Machines -II	5	-	-	5	
4030420	Measurements, Instruments and Transducers	5	-	-	5	
4040430	Analog and Digital Electronics	4	-	-	4	
4020440	E-Vehicle Technology and Policy	4	-	-	4	
4030450	Electrical Machines and Instrumentation Practical	-	-	5	5	
4040460	Analog and Digital Electronics Practical	-	-	5	5	
4030470	Electrical Circuits and Simulation Practical	-	-	4	4	
		18	-	14	32	
	Extra / Co-Curr	icular activ	vities			
Library		-	-	-	1	
Physical Education			2			
TOTAL						

TAMILNADU GOVERNMENT POLYTECHNIC COLLEGE, (AUTONOMOUS), MADURAI- 11

DIPLOMA IN ENGINEERING / TECHNOLOGY SYLLABUS

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING (PART TIME)

N - 20-SCHEME

(Implemented from the Academic year 2021-2022 onwards)

FOURTH SEMESTER

Subject	Subject	HOURS PERWEEK				
-		Theory	Tutorial/			
Code			Drawing	Practical	Total	
4030330	Electrical Machines -1	4	-	-	4	
4030350	Electrical Circuits and Machines Practical	4	-	-	4	
40025	Engineering Graphics - II	-	4	-	4	
40002	Computer Application Practical	-	-	3	3	
4030360	Electrical Workshop Practical	-	-	3	3	
	TOTAL	8	4	6	18	

N - 20 SCHEME

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

- Course Name : Diploma in Electrical and Electronics Engineering
- Subject Code : 4030410
- Semester : IV
- Subject Title : Electrical Machines II

TEACHING AND SCHEME OF EXAMINATION

No of weeks per semester: 16 weeks

	Instructions		Examination			
Subject	Hours / Hours		Marks			
	Week Semester	Internal Assessment	End Semester Examinations	Total	Duration	
ELECTRICAL MACHINES - II	5	80	25	100*	100	3 Hrs.

* Examinations will be conducted for 100 marks and it will be reduced to 75 marks.

Topics and Allocation of Hours

UNIT	Торіс	Hrs.
I	Alternator Principles and Construction	12
II	Alternator Performance and Testing	16
III	Three Phase Induction Motor	16
IV	Single Phase Induction Motor & Synchronous Motor	15
V	Maintenance of Induction Motors & Starters	14
	Test & Model Exam	7
	Total	80

RATIONALE

- This subject is classified under Core Technology group intended to teach Students about facts, concepts, Principles of Electrical Machines such as Induction Motor, Alternator and Synchronous Motor.
- Students will be able to analyze the characteristics and qualitative parameters of these Machines.
- These Machines are widely used in Industries and for generation of electricity.
- The knowledge gained by the student is useful in the study of Technological Subjects such as Utilization System, Manufacturing Processes and Testing and Maintenance of Electrical Machines.
- The knowledge and skills obtained will be helpful in discharging Technical Functions such as Supervision, Controlling and as R & D Technicians.

OBJECTIVES

. The students will be able to understand the concepts of:

- Alternator Principle, Construction and their Types, EMF Induced and Cooling Techniques of Machines
- Performance of an Alternator, Testing, Characteristics, Parallel Operation, Load Sharingetc.,
- Three Phase Induction Motor, Principle, Construction, Types, Characteristics and Applications, Starting Methods
- Single Phase Motor Types, Construction, Characteristics and Applications, Synchronous Motor, Starting, Construction, Characteristics and Applications
- Maintenance of Induction Motors and Starters.

Contents:		
Unit	Name of the Topics	Hours
	ALTERNATOR PRINCIPLES AND CONSTRUCTION Basic Principle of Alternators Types of Alternators Stationary	10
	Armatura Potating Field Advantages of Potating Field	
	Construction Dotails of Alternator Salient Pole Poter Cylindrical	
	Type Rotor - Types of A.C. Armature Windings - Types of Slots -	
	Full Pitch and ShortPitched Windings - Phase Spread Angle and Effect	
I	of Distribution Eactor - Pitch Eactor - Relation between Erequency	
	Speed and Number of Poles - EME Equation - Problems	
	Methods of obtaining Sine Wave - Critical Speed of Botor -	
	Ventilation of Turbo Alternators - Advantages of Hydrogen Cooling	2
	and its Precaution - Excitation and Exciters	2
	Load Characteristics of Alternators - Reason for Change in Terminal	
	Voltage -Qualitative Treatment of Armature Reaction for various	
	Power Factor Loads – Effective Resistance – Leakage Reactance	10
	 Synchronous Reactance, Synchronous Impedance — Voltage 	
	Regulation - Determination of Voltage Regulation of Alternator by	
П	Direct Load Test - Pre-Determination of Regulation of Alternator by	
	Indirect Method (EMF, MMF, and ZPF).	
	Necessity and conditions for Parallel Operation of Alternators -	
	Synchronizing by Dark Lamp Method, Bright Lamp Method, Dark - Bright	6
	Lamp Method and Synchro scope Method- Synchronizing Current,	
	Synchronizing Power and Synchronizing Torque - Load Sharing of	
	Alternators -Infinite Bus Bar.	
	THREE PHASE INDUCTION MOTOR	10
	Induction Motors Slip and Slip Frequency Comparison between Cage	
	and Slip Ring Induction Motors - Development of Phasor Diagram -	
	Expression for Torque in Synchronous Watts - Slin-Torque	
	Characteristics – Stable and Unstable Region – No Load Test and	
	Blocked Rotor Test - Development of Approximate Equivalent Circuit	
	- Problems on the above tonics-Simplified Circle Diagram	
	riosionis on the above topics-omplified oncie Diagram	

	Determination of Maximum Torque, Slip (Problems Not Required) -	
	Starting Torque and Starting Current Expression - Relationship between	
	Starting Torque and Full Load Torque – Speed Control of Induction	
	Motors.	
	Starters of Induction Motors - Direct online Starter and Its Merits for	6
	Cage Motors – Star Delta Starter- Auto Transformer Starter - Rotor	
	Resistance Starter - Cogging -Crawling in Induction Motor-Double	
	Cage Induction Motor-Induction Generator.	
	SINGLE PHASE INDUCTION MOTOR	8
	Single Phase Induction Motors – Not Self Starting – Methods of	
	Making itself Starting – Construction, Working Principle – Phasor	
	Diagram-Slip Torque Characteristics- Split Phase Motor - Capacitor	
	Motor - Shaded Pole Motor - Repulsion Motor - Universal Motor -	
	Operation of Three Phase Motor with Single Phase Supply.	
IV	SYNCHRONOUS MOTOR	
	Principle of Operation -Not Self Starting - Methods of Starting-Effects	
	of Excitation on Armature Current and Power Factor- "V" Curve and	7
	Inverted "V" Curve of Synchronous Motor - The Phenomenon of	
	Hunting and Prevention of Hunting by Damper Winding -	
	Comparisonbetween Synchronous Motor and Three Phase Induction	
	Motor - Applications - Problems on Power Factor Improvement.	
	MAINTENANCE OF INDUCTION MOTORS AND STARTERS	7
	BIS Publication Dealing with The Code of Practice of Induction Motors	
	and Starters - Classification of Cage Motor - Continuous Rating and	
	Intermittent Rating - Various Types of Enclosures - Specifications of	
	Motors - Selecting the Cable Rating - Single Phase Prevention using	
V	Current Operated Relay - Commissioning - Annual Maintenance	
	Selection of Starters of Induction Motor - Common Induction Motor	
	Troubles and their Remedies - Causes of Noise and Vibration - Care	7
	ofBearings - Static Balancing - Degreasing - Vacuum Impregnation -	/
	Varnishing – Effect of Unbalanced Supply on the Performance of	
	Induction Motor.	

TEXTBOOK

S.No	Author	Title	Publisher
1.	B.L. Theraja	A Textbook of Electrical Technology -Volume II	S.Chand& Co. New Delhi
2.	Edward Hughes	Electrical Technology	Addision- Wesley International Student Edition

REFERENCE BOOK

S.No	Author	Title	Publisher
1.	M.G.Say	Performance and Design of ACMachines	Pitman PublishingLtd
2.	Nagarath	Electrical Machines	TMH Publications
3.	Bhattacharya	Electrical Machines	TMH Publications

TAMILNADU GOVERNMENT POLYTECHNIC COLLEGE, (Autonomous), MADURAI- 11 N - 20 SCHEME

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

- Course Name : Diploma in Electrical and Electronics Engineering
- Subject Code 4030420
- Semester : IV Semester
- Subject Title : MEASUREMENTS, INSTRUMENTS AND TRANSDUCERS

TEACHING AND SCHEME OF EXAMINATION

No. of weeks per Semester: 16 Weeks

Subject	Instruc	ction	Examination			
	Hours/	Hours/	Mai	rks		Duration
MEASUREMENTS, INSTRUMENS AND	Week	Semester	Internal Assessment	End Semester Examination	Total	
IRANSDUCERS	5	80	25	100*	100	3 Hrs

* Examinations will be conducted for 100 marks and it will be reduced to 75 marks.

TOPICS AND ALLOCATION OF HOURS

Unit	Торіс	Hours
I	Classification and Characteristics of Instruments	15
П	Measurement of current Voltage and Resistance	15
	Measurement of Power, Power Factor and Frequency	15
IV	Measurement of L, C Parameters, Waveforms and Signal Conditioners	13
V	Sensors and Transducers	15
	Revision and Test	07

Total	80
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RATIONALE

Measurement is the basic and primary operation, the result of which is used only to describe the system and hence treated as an independent operation. Automation of any kind begins with the measurement of certain system parameters; In fact, Industrial growth moves hand in hand with the growth of the measurement of Science and Technology. Therefore, it is highly essential for Electrical Students to study about the measurement of various Electrical Parameters in a system and the construction and working of different Instruments used in measurement of such parameters.

OBJECTIVES

At the end of the Semester, Students will be able to:

- To define basic measurement terms.
- To learn about various operating Forces and effects used in Instruments.
- To study the construction and working of Moving coil and Moving Iron instruments, CT and PT and Electrostatic Voltmeter.
- To understand the measurement of Resistance using different means.
- To study Single Phase and Three Phase Power Measurement using Wattmeter.
- To study the construction and working of Single Phase, Three Phase Energy Meter and study about calibrations.
- To study the construction and working of Power Factor Meters, and Phase Sequence Indicators.
- To study about the Frequency Measurement using different types of Frequency Meters.
- To learn about the measurement of Inductance and Capacitance using Bridges.
- To study about CRO and its applications.

CONTENTS: Theory

UNIT	NAME OF THE TOPICS	HOUR S				
	CLASSIFICATION AND CHARACTERISTICS OF INSTRUMENTS					
	General - Definition of Measurement - Functions of Measurement					
	System (Indicating, Recording and Controlling Function) –	10				
	Applications of Measurement Systems - Classification - Absolute					
I	and Secondary Instruments - Indicating Recording and Integrating					
	Instruments -Analog and Digital					
	Definition of True Value, Accuracy, Precision, Error and Error	F				
	Correction – Instrument Efficiency – Effects used in	5				
	Instruments – Operating Forces – Deflecting, Controlling and					
	Damping Forces.					
	MEASUREMENT OF CURRENT, VOLTAGE AND RESISTANCE					
	Types of Instruments — Construction, Working and Torque					
	Equation of Moving Coil, Moving Iron, Dynamometer Type	10				
	(Shaded Pole) Instruments – Extension of Instrument Range					
	Using Shunts and Multipliers. (Calculation, Requirements and					
	Simple Problems). Tong Tester — Electrostatic Voltmeter —					
	Rectifier Type Instruments -Instruments Transformers CT and PT					
II	- Testing, Errors and Characteristics of CT and PT.					
	Classification of Resistance - Measurement using Conventional	F				
	Method – (Ammeter – Voltmeter Method) Measurement of Low	5				
	Resistance using Kelvin [®] s Bridge Ohmmeter - Measurement of					
	Medium Resistance using Wheatstone Bridge – High					
	Resistance using Megger - Earth Resistanceu Earth Tester -					
	Multimeters.					

	MEASUREMENT OF POWER, POWER FACTOR AND FREQUENCY	
	Power in D.C. and A.C. Circuits — Watt Meters in Power	
	Measurement —Measurement of Energy in AC Circuits – Single	
	Phase and Three Phase Energy Meters Construction and	7
	Operation — Digital Energy Meter.	
Ш	Power Factor Meters - Single Phase Electro Dynamometer Type	
	- Construction and Working - Phase Sequence Indicator - Phase	
	Difference Measurement using Synchro scope -Tri-vector Meter -	8
	Merz Price Maximum Demand Indicator. Frequency Measurement	
	Frequency Meter – Digital Frequency Meter (Simplified Block	
	Diagram)	
	MEASUREMENT OF L, C PARAMETERS, WAVEFORMS AND SIGNAL CONDITIONERS	
		_
	Inductance – Maxwell [®] s Inductance Bridge – Andersons Bridge -	5
	Measurement of Capacitance using Schering Bridge.	
IV	CRO – Block Diagram – CRT – Applications - Measurements	
	of Voltage, Frequency and Phase Difference Using CRO - Digita	5
	Storage Oscilloscope – Block Diagram.	
	SIGNAL CONDITIONER: Basic Components of Signal	3
	Conditioning System	
	SENSORS AND TRANSDUCERS	
	Definition - Types of Transducers	8
	Resistive Transducer - Strain Gauge - Capacitive Transducer -	
	Inductive Transducer - Proximity Sensor - Construction and	
	Operation of LVDT and RVDT	
V		
	RTD - Thermistor - Thermocouple - Synchrous - Piezoelectric	
	Transducer-Measurement of Pressure and Vibration - Hall Effect	
	Transducer - Photovoltaic Transducer - Photoconductive	7
	Transducer.	
	TELEMETRY: Block Diagram and its Applications	

TEXT BOOKS:

S.No	Title	Author	Publishers
1.	A Course in Electrical	A.K. Sawhney	Puneet Sawhney
	and Electronics Measurements		Dhanpat Rai & Co (P)
	and Instrumentation		Ltd., New Delhi 1993

REFERENCE BOOKS:

S.No	Title	Author	Publishers
1.	Electronic	HS Kalsi	Tata Mc Graw Hill Publishing
	Instrumentation		Co., Delhi 2010
2.	Modern Electronic	Albert D. Helfrick	Prentic - Hall of India
	Instrumentation and	William David	(P)Ltd., New Delhi 2010
	Measurement techniques	Cooper	
3.	Electronics and	Dr.S.K.Battachariya	S.K. Kataria & Sons, New
	Instrumentation	Dr. Renu Vig	Delhi
4.	A course in Electrical and	Umesh Sinha	Satya Prakashan,
	Electronic Measurement		New Delhi
	and Instrumentation		

N - 20 SCHEME

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

4040430

IV

Course N	lame
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: All Branches of Diploma in Engineering and Technology

Subject Code

Semester

Subject title

: ANALOG AND DIGITAL ELECTRONICS

TEACHING AND SCHEME OF EXAMINATION

No of weeks/ semester: 16weeks

	Instruction		Examination				
Subject	Hours Hours		Marks				
	/Week	/Semester	Internal	End	Total	Duration	
			Assessment	Semester			
				Examination			
ANALOG AND DIGITAL ELECTRONICS	4	64	25	100*	100	3 Hrs	

* Examination will be conducted for 100 marks and it will be reduced to 75 marks.

Topics and Allocation of hours

Unit	Торіс	Hrs
I	Linear ICs and OP-amps	10
II	A/D, D/A, Special Function ICs and IC Voltage Regulators	13
	Boolean Algebra and Arithmetic operations	10
IV	Combinational and Sequential Logic Circuits	12
V	Memories	12
	Tests and Model Exam	7
	Total	64

RATIONALE:

The subject Analog and Digital Electronics holds applications in all branches of engineering instrumentation and Industrial Automation. This will impart in depth knowledge of Number Systems, Logics of Combinational &Sequential circuits and memories.

OBJECTIVES:

On completion of the following units of the syllabus contents, the students must be able to

- > Understand the basics of operational amplifier.
- Know the op-amp applications.
- > Know the waveform generator and Active filter.
- Know the concept of D/A and A/D converters
- > Know the applications of Special function IC, IC 555 Timer.
- Understand various Number Systems used in Digital Circuits
- Understand basic Boolean postulates and laws.
- > Understand the De-Morgan"s theorem.
- Understand the concept of Karnaugh Map.
- Learn about Basic logic Gates.
- Study about Boolean techniques.
- Learn the different digital logic families
- > Learn arithmetic circuits- Adder/Subtractor
- Understand the encoder/decoder & MUX / DEMUX
- > Understand the concept of parity Generator and checker
- > Understand various types of flip-flops.
- Understand various types of counters
- Understand various modes of shift registers
- Understand various types of memories

Contents: Theory

Unit	Name of the topics	Hours
	LINEAR ICS AND OP-AMPS 1.1: OPERATIONAL AMPLIFIER	4
I	Ideal Op-Amp - Block diagram and Characteristics - Op-amp parameters CMRR - Slew rate - Concept of Virtual ground : APPLICATIONS OF OP-AMP Inverting amplifier -Summing amplifier - Non inverting amplifier - Voltage	4
	 follower - Comparator - Zero crossing detector - Integrator - Differentiator- waveform generation (Schmitt Trigger only)-RC Low pass Active filter. : OP-AMP SPECIFICATIONS OP-amp 741 - Symbol - Pin diagram - Specifications 	2
	A/D, D/A, SPECIAL FUNCTION ICs AND IC VOLTAGE REGULATORS 2.1: SAMPLING AND QUANTIZATION	2
	: A/D CONVERTER Analog to digital conversion using Ramp method - Successive approximation method - Dual slope method - Specifications of A/D converter : D/A CONVERTER	3
11	Basic concepts - Weighted Resistor D/A converter - R-2R Ladder D/A converter - Specifications of DAC IC : SPECIAL FUNCTION ICs	2
	: IC 555 Timer – Pin diagram - Functional Block diagram of IC 555 in Astable and Monostable Multivibrator mode - Schmitt trigger using IC 555 2.4.2: IC 565-PLL-Pin Diagram-Functional Block diagram of IC 565	3
	 2.4.3: IC 566-VCO-Pin Diagram-Functional Block diagram of IC 566 2.5.: IC VOLTAGE REGULATORS Positive IC Voltage Regulators: 78XX - Negative IC Voltage Regulators: 79XX and General-purpose IC Voltage Regulators using LM 723. 	3

	BOOLEAN ALGEBRA AND ARITHMETIC OPERATIONS	
	3.1: NUMBER SYSTEMS	2
	Decimal – Binary – Octal – Hexadecimal – BCD – Conversion from one	Z
	number system to other - Boolean Algebra - Basic laws and Demorgan"s	
	Theorems	
	: UNIVERSAL GATES	2
	Realization of basic logic gates using universal gates NAND and NOR -Tristate	
	Buffer circuit	
	: PROBLEMS USING 2, 3, AND 4 VARIABLES	2
	Boolean expression for outputs - Simplification of Boolean expression using	
	Karnaugh map (up to 4 variable)- Constructing logic circuits for the Boolean	
	expressions	2
	:ARITHMETIC OPERATIONS	
	Binary Addition-Binary Subtraction-1"s compliment and 2"s compliment-Signed	
	binary numbers	2
	: ARITHMETIC CIRCUITS	
	Half Adder-Full Adder-Half Subtractor-Full Subtractor	
	COMBINATIONAL AND SEQUENTIALLOGIC CIRCUITS	
	4.1: PARITY GENERATOR AND CHECKER	
	: DECIMAL to BCD ENCODER	1
	: 3 to 8 DECODER	1
	: MULTIPLEXER: 4 to 1 Multiplexer	•
	: DEMULTIPLEXER :1 to 4 Demultiplexer	1
IV	: FLIP-FLOPS (FF)	1
	RS FF- JK FF: Master Slave FF and Edge triggered FF - D and T FF	
	: COUNTERS	1
	4 bit Asynchronous Up Counter - Mod N counter - Decade counter -	2
	4 bit Synchronous up counter	-
	4.8: SHIFT REGISTER	3
	4 bit shift register - Serial in Serial out	2
		-

MEMORIES

: CLASSIFICATION OF MEMORIES

:RAM

V

RAM organization-Address Lines and Memory Size- Read/write operations-Static RAM-Bipolar RAM cell- Dynamic RAM- SD RAM- DDR RAM.

:ROM

ROM organization-Expanding memory- PROM- EPROM- and EEPROM- Flash 6 memory- Anti Fuse Technologies.

6

TEXT BOOKS:

- 1. Roger L. Tokheim Macmillan Digital Electronics McGraw Hill 1994.
- D.Roychoudhury & shail. B.Jain- Linear Integrated Circuits -New age International publishers - II Edition -2004.

REFERENCE BOOKS:

- 1. Albert Paul Malvino and Donold P. Leach Digital Principles and Applications
- 2. William H.Goth Mann Digital Electronics An introduction to theory and practice PHI 1998.
- 3. Linear Integrated Circuits by B.Suseela & T.R.Ganesh babu -Scitech publications-2018
- 4. Integrated circuits by K.R.Botkar-Khanna publisher^s-1996.
- 5. R.P.Jain Modern Digital Electronics TMH 2003.

N - 20 SCHEME

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

- Course Name : All branches of Diploma in Engineering and Technology
- Subject Code : 4020440
- Semester : VI
- Subject Title : E VEHICLE TECHNOLOGY & POLICY

TEACHING AND SCHEME OF EXAMINATION

No of Weeks per Semester: 16 Weeks

	Instru	uctions		Examination		
Subject	Hours /	s / Hours / k Semester	Marks			
-	Week		Internal Assessment	End Semester Examinations	Total	Duration
E - VEHICLE TECHNOLOGY AND POLICY	4	64	25	100*	100	3 Hrs.

* Examinations will be conducted for 100 marks and it will be reduced to 75 marks.

Topics and Allocation of Hours

Unit	Торіс	Hours
Ι	Environmental impact and history& Electric vehicle Types	12
II	Electric vehicle & Drive System	12
=	Energy Storages, Charging System, Effects and Impacts	12
IV	Electric Mobility Policy Frame work India	11
V	Tamilnadu E-Vehicle Policy 2019	10
	Test & Model Exam	7
	Total	64

RATIONALE

The world is transitioning to cleaner mobility options with the aim at improving air quality and reducing dependency on fossil fuels. Electric Vehicles (EVs) have emerged a popular clean mobility choice to reduce emissions. EVs are powered fully or partially by batteries, they can help to reduce dependence on fossil fuels also air quality. Tamil Nadu is one of the most advanced states in India. Tamil Nadu has a highly developed industrial eco-system and is very strong in sectors like automobiles and auto-components. Many globally renowned companies have setup their manufacturing facilities in Tamil Nadu. Due the rapid depletion of fossil fuel and increase in fuel cost, environmental pollution, the shift to clean transport is necessary. This subject introduced by keeping all the above factors.

OBJECTIVES

- 1. To learn the environmental impact and history of Electric Vehicles.
- 2. To understand the concept of Electric Vehicle and its types.
- 3. To study the configurations of Electric Vehicles
- 4. To acquire knowledge about Energy Storages, Charging System, Effects and Impacts
- 5. To appreciate the Electric Mobility Policy Frame work India and EV Policy Tamil Nadu 2019.

Contents: Theory

Unit	Name of the Topics	Hours
	Environmental impact and history& Electric vehicle Types:	12
I	Environmental impact and history: Air pollution - Petroleum resources	
	- History of Electric vehicles - History of Hybrid Electric Vehicles -	
	Historyof Fuel Cell Vehicles - Hybrid electric Vehicle (HEV) - Plug-in	
	Hybrid Electric Vehicle (PHEV) - Battery Electric Vehicle (BEV) -	
	Fuel Cell Electric Vehicle (FCEV) - Description.	
	Electric vehicle & Drive System:	
	Electric Vehicles: Configurations of Electric Vehicle – Performance	
	ofElectric Vehicles— Tractive Effort in Normal Driving — Energy	
	Consumption. Hybrid Electric Vehicles: Concept of Hybrid Electric Drive	
11	Trains – Architecture of Hybrid Electric Drive Trains. Electric	12
	Propulsion Systems: Drive Systems: DC Motor Drives - Principle of	
	Operation – Induction Motor Drives - Basic Operation Principles –	
	Permanent Magnetic Brush Less DC Motor Drives – Principles –	
	Construction and Classification.	
	Energy Storages, Charging System, Effects and Impacts:	8
	Energy Storages: Electrochemical Batteries - Battery Technologies -	
ш	Lead Acid Batteries - Nickel Based Batteries - Lithium Based Batteries	
	 Charging system -DC charging - Wireless charging - Power 	
	conversiontechniques.	4
	Effects of EV - Impacts on Power grid - Impacts on Environment — Impacts on Economy.	
	Electric Mobility Policy Frame work India:	11
	Government of India Electric Mobility Policy Frame Work - Global	
IV	Scenario of EV Adoption - Electric Mobility in India - National Electric	
	Mobility Mission Plan 2020 -Action led by Original Equipment	
	Manufacturers - Key Performance Indicator - Global Impact -	
	Trends and Future Developments.	

	Tamilnadu E-Vehicle Policy 2019:	5
	Tamilnadu E-vehicle Policy 2019: Vehicle Population in Tamilnadu -	
	Need of EV Policy – Advantage of EV Eco system – Scope and	
V	Applicability of EV Policy - Objectives of EV Policy - Policy Measures	
V	-Demand Side Incentives - Supply Side Incentives to promote EV.	
	Manufacturing - Revision of Transport Regulation of EV - City	5
	Building Codes — Capacity Building and Skilling — Charging	
	Structure – Implementing Agencies – R&D and Business	
	Incubation - Recycling Ecosystem - Battery and EVs.	

TEXT BOOKS:

- 1. Modern Electric, Hybrid Electric and Fuel Cell Vehicles, Mehrdad Ehsani, Yimin Gao, Sebastien E.Gay, Ali Emadi, CR Press, London, New York.
- Comparison of Electric and Conventional Vehicles in Indian Market: Total Cost of Ownership, Consumer Preference and Best Segment for Electric Vehicle (IJSR), Akshat Bansal, Akriti Agarwal

REFERENCE BOOKS:

- 1. A Comprehensive Study of Key Electric Vehicle (EV) Components, Technologies, Challenges, Impacts, and Future Direction of Development (MDPI), Fuad Un-Noor, Sanjeevi kumar Padmanaban, Lucian Mihet-Popa, Mohammad NurunnabiMollah and Eklas Hossain.
- 2. Electric Vehicles: A future Projection CII October 2020 report.
- Design and analysis of aluminum/air battery system for electric vehicles, Shaohua Yang, Harold Knickle, Elsevier.
- 4. Propelling Electric Vehicles in India, Technical study of Electric Vehicles and Charging Infrastructure
- 5. Zero Emission Vehicles (Zevs): Towards A Policy Framework Niti Aayog.
- 6. Faster Adoption of Electric Vehicles in India: Perspective of Consumers and Industry, The Energy and Resources Institute, New Delhi.
- 7. India EV Story: Emerging Opportunities by Innovation Norway.

N - 20 SCHEME

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

- Course Name : Diploma in Electrical and Electronics Engineering
- Subject Code : 4030450
- Semester : IV

Subject Title : Electrical Machines and Instrumentation Practical

TEACHING AND SCHEME OF EXAMINATION

No of weeks per semester: 16 weeks

	Instructions		Examination			
Subject	Hours / Hou Week Sem	Hours /	Marks			
		Semester	Internal Assessment	End Semester Examinations	Total	Duration
ELECTRICAL MACHINES AND INSTRUMENTATI ONPRACTICAL	5	80	25	100*	100	3 Hrs.

* Examinations will be conducted for 100 marks, and it will be reduced to 75 marks.

RATIONALE:

- To impart practical knowledge to the Diploma Students, Practical[®]s are introduced for every corresponding Theory Subject.
- This Practical Subject supports the Aim and Objective of Electrical Machines II and Measurements and Instruments subjects.

OBJECTIVES:

On completion of this practical Subject the Students will be able to:

- Understand the characteristics of AC Machines.
- Make various Electrical Measurements.
- Use Transducers in Non-Electrical Quantity Measurement

Contents: Practical

Name of the Topics: Electrical Machines and Instrumentation Practical

Exercise:

- 1. Predetermine the Regulation of Alternator.
- 2. Load test on 3 Phase Alternator.
- 3. Synchronization of 3 Phase Alternator
- 4. Load test on Single Phase Induction Motor.
- 5. Load test on 3 Phase Induction Motor.
- 6. Determine the Equivalent Circuit Constants of 3 Phase Induction Motor.
- 7. Predetermine the performance of a 3 Phase Induction Motor.
- 8. Improvement of Power Factor of an Induction Motor with load.
- 9. Calibration of given Ammeter and Voltmeter.
- 10. Calibration of given Wattmeter.
- 11. Calibration of 3 Phase Energy Meter.
- 12. Measurement of Alternator Winding Resistance using Wheatstone Bridge
- 13. Measurement of value of unknown Capacitance using Schering Bridge.
- 14. Measurement of value of unknown Inductance using Anderson Bridge.
- 15. Displacement measurement using LVDT.
- 16. Measurement of earth Resistance by using Megger.

LIST OF EQUIPMENTS (For a Batch of 30 Students)

S NO		QUANTITY		
3.110	NAME OF TH EQUIPMENTS	REQUIRED		
1	Three Phase Squirrel Cage Induction motor 5 HP, 440V, 1440 rpm	2		
1.	with starting and loading arrangement	۷		
2	Three Phase Squirrel Cage Induction motor 5 HP,440V,1440 rpm	1		
Ζ.	without starting and loading arrangement	I		
2	Three phase Slip ring Induction motor 5HP, 440V, 940/1450 rpm with	1		
5.	starting and loading arrangement	I		
1	Single phase induction motor with staring and loading arrangement	1		
ч.	2HP, 250V, 10A, 1440 rpm.	I		
5.	Wheatstone bridge.	2		
6.	Anderson Bridge.	2		
7.	Schering Bridge.	2		
8.	1 Phase Energy meter induction type, 250V, 10A.	2		
9.	3 Phase Energy meter Induction type 440V, 10/20A.	2		
10.	Earth megger with necessary connecting leads and rods.	1		
11.	3 phase Alternator with prime mover.	2		
12.	Synchronizing panel.	1		
13.	PF meter (power factor meter).	2		
14.	LVDT trainer.	2		
15.	3 phase capacitor bank rating of 1KVAR, 400/440 V.	1		

DETAILLED ALLOCATION OF MARKS

S.NO	NAME OF THE ACTIVITY	MARK ALLOCATION
1	Circuit Diagram	30
2	Connections And Conduction of the Experiment	30
3	Reading/Calculation	20
4	Graph/Result	15
5	Viva Voce	05
	Total	100

N - 20 SCHEME DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

- Course Name : All Branches of Diploma in Engineering and Technology
- Subject code : 4040460
- Semester : IV

Subject title : ANALOG AND DIGITAL ELECTRONICS PRACTICAL

TEACHING AND SCHEME OF EXAMINATION

No. of weeks/ Semester: 16weeks

	Instruction		Examination			
			Marks			
Subject	Hours /week	Hours /semester	Internal Assessment	End Semester Examination	Total	Duration
ANALOG AND DIGITAL ELECTRONICS PRACTICAL	5	80	25	100*	100	3 Hours

* Examination will be conducted for 100 marks and it will be reduced to 75 marks.

RATIONALE:

Every Electronic Engineer should have sound knowledge about the ICs used in Electronics Industry. This is vital in R&D Department for Chip level troubleshooting. To meet the industrial needs, diploma holders must be taught about the most fundamental subject, Analog and Digital Electronics Practical. By doing practical experience in this, they will be skilled in handling all types of ICs and able to apply the skill in electronic system design and the designing of PCBs.

OBJECTIVES:

On completion of the following experiments, the students must be able to

- > Know the Verification of truth table of OR, AND, NOT, NOR, NAND, EX-OR gates
- > Know the Realization of basic gates using NAND & NOR gates.
- > Know the verification of Half Adder and Full Adder using IC's.
- > Know the verification of Half Subtractor and Full Subtractor using IC's.
- > Know the Verification of Truth Table for Decoder/Encoder.

- > Know the Verification of truth table for RS, D, T & JK flip-flop.
- Test Inverting Amplifier and Non inverting amplifier using Op-amp
- > Test Summing Amplifier, Difference Amplifier and Voltage Comparator using Op-amp.
- > Test Integrator and Differentiator.
- > Test Astable multivibrator using IC 555
- > Design IC Voltage Regulator Power Supplies using IC 7805, IC 7912
- > Design the PCB of 4- bit ripple counter using FF

4040460 ANALOG AND DIGITAL ELECTRONICS PRACTICAL DETAILED SYLLABUS

Contents: Practical

Exercises

Note: At least 6 experiments should be constructed using breadboard

- 1. Realization of basic gates using NAND & NOR gates.
- 2. Realization of logic circuit for De-Morgans Theorems
- 3. Test the performance of Half Adder and Full Adder.
- 4. Test the performance of Half Subtractor and Full Subtractor.
- 5. Test the performance of Decoder/Encoder.
- 6. Test the performance of RS, D, T & JK flip-flops.
- 7. Test the performance of Parity generator and checker using parity checker/ generator IC's.
- 8. Test the performance of Multiplexer/De-multiplexer using IC 4051
- Test the performance of Inverting Amplifier and Non inverting amplifier using Op-amp IC 741.
- 10. Test the performance of Summing Amplifier, Difference Amplifier.
- 11. Test the performance of Zero Crossing Detector and Voltage Comparator using Op-amp IC 741.
- 12. Test the performance of Integrator and Differentiator using Op-amp IC 741.
- 13. Test the performance of Astable multivibrator using IC 555.
- 14. Test the performance of IC Voltage Regulator Power Supplies using IC 7805, IC 7912.
- 15. Design the PCB of 4- bit ripple counter using FF using Software tool Multisim/OrCAD

DETAILED ALLOCATION OF MARKS

S.No.	NAME OF THE ACTIVITY	MARK ALLOCATION
1	CIRCUIT DIAGRAM	25
2	CONNECTION	30
3	EXECUTION & HANDLING OF EQUIPMENT	20
4	OUTPUT / RESULT	15
5	VIVA - VOCE	10
	TOTAL	100

LIST OF EQUIPMENTS (For a Bach of 30 Students)

S. NO	Name of the Equipments	Range	Required Nos
1	DC Regulated power supply	0-30V,1A	5
	IC Voltage Power Supply	0-5V,1A	5
2		15-0-15V, 1A	5
3	Signal Generator	1MHz	4
4	Dual trace CRO	20MHz/ 30MHz	5
5	Digital Trainer	-	10
6	DC Voltmeter (Analog/Digita)	Different Ranges	5
7	DC Ammeter (Analog/Digital)	Different Range	5
8	Desk Top Computer	-	5
9	Simulation Tool	Multisim/OrCAD	1

N - 20 SCHEME

DIPLOMA IN ELECTRICAL AND ELECTRONICS ENGINEERING

(Implemented from the Academic Year 2021 - 2022 onwards)

- Course Name : Diploma in Electrical and Electronics Engineering
- Subject Code : 4030470
- Semester : IV

Subject Title : Electrical Circuits and Simulation Practical

TEACHING AND SCHEME OF EXAMINATION

No of weeks per semester: 16 weeks

Ir		uctions	Examination			
Subject Hours / H		Hours /	Marks			
	Week	Semester	Internal Assessment	End Semester Examinations	Total	Duration
ELECTRICAL CIRCUITS AND SIMULATION PRACTICAL	4	64	25	100*	100	3 Hrs.

* Examinations will be conducted for 100 marks, and it will be reduced to 75 marks.

RATIONALE

All the Engineering applications are simulated through Computers. They are tested and then built using real components for commercial implementation. Simulation Software is available for all Engineering Fields. Hers is an attempt to impart the knowledge of using Simulation Software for realizing some of the Electrical and Electronics Circuits for the Diploma students.

OBJECTIVES

On Completion Of This Practical subject, the Students will be able to know :

- ✓ The Various Aspects of Simulation Software
- ✓ Simulate and Test the Simple Electrical and Electronics Circuits
- ✓ Simulate and Test the Wave Generating Circuits

- ✓ Simulate and Prove the Simple Theorems
- ✓ Simulate and Test the Performance Characteristics of Converters
- ✓ Design and Verify the Results of Various Electric Circuits Using Simulation Software

Contents: Practical

Name of the Topics: Electrical Circuits and Simulation Practical Exercise

- 1. Generate the following waveforms
 - (i) Sinusoidal waveform of Fundamental Frequency (50Hz)
 - (ii) 3rd Order, 5th Order and 7th Order Harmonics for the Fundamental frequency.
- 2. Simulation of RLC series and RLC Parallel Response Circuits.
- 3. Step Response of RL and RC Series Circuit.
- 4. Simulation of Mesh and Nodal analysis for DC Circuits.
- 5. Verification of Superposition Theorem.
- 6. Verification of Thevenin"s and Norton"s Theorem.
- 7. Verification of Maximum Power Transfer Theorem.
- 8. Simulation of Full Wave Rectifier (Center Tapped and Bridge) with RL load.
- Simulation of Single-Phase Half Wave Controlled Converter with RL Load and FreeWheeling Diode.
- 10. Simulation of Single-Phase Full Wave Controlled Converter with RL Load and FreeWheeling Diode.
- 11. Simulation of Three Phase Star Connected Balanced and Unbalanced Load
- 12. Simulation of Three Phase Delta Connected Balanced and Unbalanced Load
- 13. Simulation of Three Phase Non-Linear Star Connected Load with Three Phase 3 WireSystem.
- 14. Simulation of Three Phase Non-Linear Star Connected Load with Three Phase 4 Wire System.
- 15. Simulation basic Logic Gates, Universal Logic Gates and Realization of Logic Gates usingUniversal Logic Gates.
- 16. Simulation of Half Adders and Full Adder.

LIST OF EQUIPMENTS (FOR A BATCH OF 30 STUDENTS)

S.No.	LISTOFEQUIPMENTS	QUANTITYREQUIRED
1.	PC with any suitable simulation software	30
2.	UPS 5KVA with half an hour battery backup	1
3.	Printer	1

DETAILED ALLOCATION OF MARKS

S.NO	NAME OF THE ACTIVITY	MARK ALLOCATION
1	Circuit Diagram (Manual Diagram)	30
2	Development of circuit diagram	30
3	Simulation Performance & print out	35
4	Viva Voce	05
	Total	100